## Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A semiconductor integrated circuit having a surfaceemitting laser, comprising:

a transparent substrate;

the surface-emitting laser composed of a different material than the transparent substrate, the surface-emitting laser layer adhered to the transparent substrate by an adhesive; and

an integrated circuit chip that is flip-chip mounted on the transparent substrate and arranged to cover the surface-emitting laser; the integrated circuit chip including a light receiving device that is arranged so as to face the surface-emitting laser.

2. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the light receiving device being a photodiode.

3. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 2,

the photodiode being an MSM photodiode.

4. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

a light receiving part of the light receiving device being positioned on an optical axis of the surface-emitting laser.

5. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the integrated circuit chip including an auto power control circuit that controls an amount of light emitted by the surface-emitting laser based on an amount of light detected by the light receiving device.

6. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the integrated circuit chip including a signal processing circuit and an output signal of the signal processing circuit being an input signal to the surface-emitting laser.

7. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the transparent substrate including an auto power control circuit that controls an amount of light emitted by the surface-emitting laser based on an amount of light detected by the light receiving device.

8. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the transparent substrate including signal processing circuit and an output signal of the signal processing circuit being an input signal to the surface-emitting laser.

9. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the transparent substrate including a lens that is positioned on an optical axis of the surface-emitting laser.

10. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 9,

the surface-emitting laser being adhered to a surface of the transparent substrate and the lens being provided to a back surface of the transparent substrate.

11. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the transparent substrate including a diffraction grating that is positioned on an optical axis of the surface-emitting laser.

12. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 11,

the surface-emitting laser being adhered to a surface of the transparent substrate and the diffraction grating being provided to a back surface of the transparent substrate.

13. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 1,

the light receiving device having wavelength selectivity.

14. (Original) The semiconductor integrated circuit having a surface-emitting laser according to Claim 13,

a light receiving part of the light receiving device being provided with a filter that transmits only light of a predetermined wavelength.

- 15. (Canceled)
- 16. (Previously Presented) The semiconductor integrated circuit having a surfaceemitting laser according to Claim 1,

the surface-emitting laser including a lower multilayered reflective layer, an active layer that is provided above the lower multilayered reflective layer, and an upper multilayered reflective layer that is provided above the active layer.

17-22. (Canceled)